



CHB200W12 Series

Application Note V16 October 2025

ISOLATED DC-DC CONVERTER CHB200W12 SERIES APPLICATION NOTE



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1. Introduction

The CHB200W12 series of DC-DC converters offers 200 watts of output power @ single output voltages of 12, 15, 24, 48VDC with industry standard half-brick. It has a wide (12:1) input voltage range of 14 to 160VDC (72VDC nominal) and 3000VAC reinforced isolation.

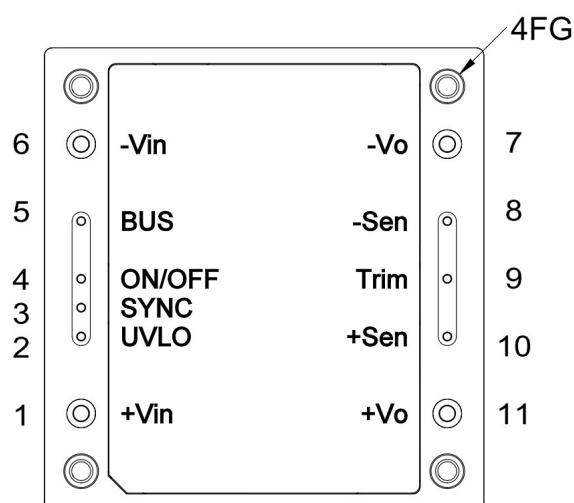
Compliant with EN 50155, EN 45545, EN 50121-3-2. High efficiency up to 90%, allowing case operating temperature range of -40°C to 100°C. An optional heat sink is available to extend the full power range of the unit.

The standard control functions include remote **on/off** (positive or negative) and +15%, -20% adjustable output voltage.

Fully protected against input UVLO (under voltage lock out), output over-current, output over-voltage and over-temperature and continuous short circuit conditions.

CHB200W12 series is designed primarily for common railway applications of 24V, 36V, 48V, 72V, 96V, 110V nominal voltage and also suitable for distributed power architectures, telecommunications, battery operated equipment and industrial applications.

2. Pin Function Description



No	Label	Function	Description	Reference
1	+Vin	+V Input	Positive Supply Input	Section 7.1/7.2/ 7.3
2	UVLO	UVLO	Under Voltage Lock Out	Section 6.1
3	SYNC	Synchronized	Output Ripple Frequency Synchronized	Section 7.6
4	ON/OFF	On/Off	External Remote On/Off Control	Section 6.5
5	BUS	BUS	Pre-Regulator Voltage Output	Section 7.2
6	-Vin	-V Input	Negative Supply Input	Section 7.1/7.2/7.3
7	-Vo	-V Output	Negative Power Output	Section 7.4/7.5
8	-Sen	-Sense	Negative Output Remote Sense	Section 6.6
9	Trim	Trim	External Output Voltage Adjustment	Section 6.7
10	+Sen	+Sense	Positive Output Remote Sense	Section 6.6
11	+Vo	+V Output	Positive Power Output	Section 7.4/7.5
12	--	Mounting Insert	Mounting Insert (FG)	Section 9.5/10.2

Note: Base plate can be connected to FG through M3 threaded mounting insert. Recommended torque 3Kgf

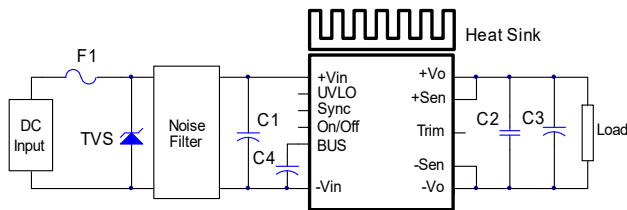


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3. Connection for Standard Use

The connection for standard use is shown below. An external input capacitor (C1) 100uF for all models is recommended to reduce input ripple voltage. The BUS capacitor (C4) is recommended to set 240uF (VISHAY 118 AHT series). External output capacitors (C2, C3) are recommended to reduce output ripple and noise, 10uF aluminum and 1uF ceramic capacitors for 48V_{out}, and 10uF tantalum and 1uF ceramic capacitors for other models.



Symbol	Component	Reference
F1, TVS	Input fuse, TVS	Section 10.1
C1	External capacitor on the input side	Note Section 7.2
C2, C3	External capacitor on the output side	Section 7.4/7.5
C4	External capacitor connected between BUS and -Vin is necessary to keep operating stable	Section 7.1/7.2
Noise Filter	External input noise filter	Section 10.2
UVLO	External UVLO adjustment	Section 6.1
Sync	External clock frequency sync	Section 7.6
Remote On/Off	External remote on/off control	Section 6.5
BUS	External hold up time	Section 7.2
Trim	External output voltage adjustment	Section 6.7
Heat Sink	External heat sink	Section 9.2/9.3/9.4/9.5
+Sense/-Sense	--	Section 6.6

Note:

If the impedance of input line is high, C1, C4 capacitance must be more than above. Use more than two recommended capacitor above in parallel when ambient temperature becomes lower than -20°C.

4. Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown below. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate:

- Efficiency
- Load regulation and line regulation

The value of efficiency is defined as:

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where:

V_o is output voltage
I_o is output current
V_{in} is input voltage
I_{in} is input current

The value of load regulation is defined as:

$$\text{Load reg.} = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where:

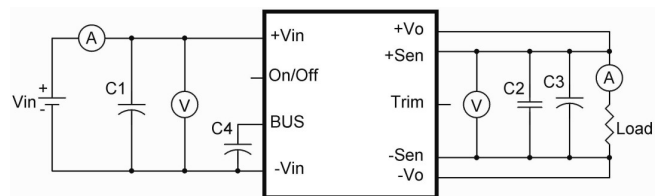
V_{FL} is the output voltage at full load
V_{NL} is the output voltage at no load

The value of line regulation is defined as:

$$\text{Line reg.} = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where:

V_{HL} is the output voltage of maximum input voltage at full load
V_{LL} is the output voltage of minimum input voltage at full load



CHB200W12 Series Test Setup

C1: 100uF/200V ESR<0.047Ω

C2: 1uF/1210 ceramic capacitor

C3: 10uF tantalum capacitor (10uF aluminum capacitor for 48V_{out})

C4: 240uF (VISHAY 118 AHT series)

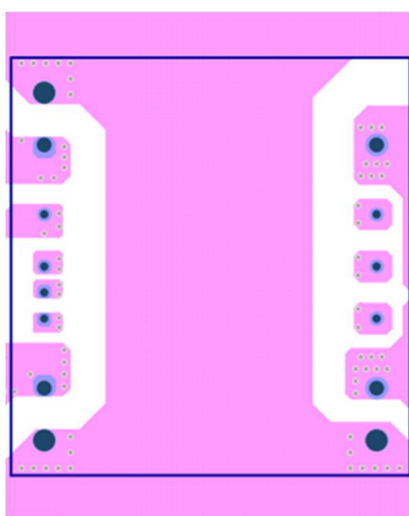
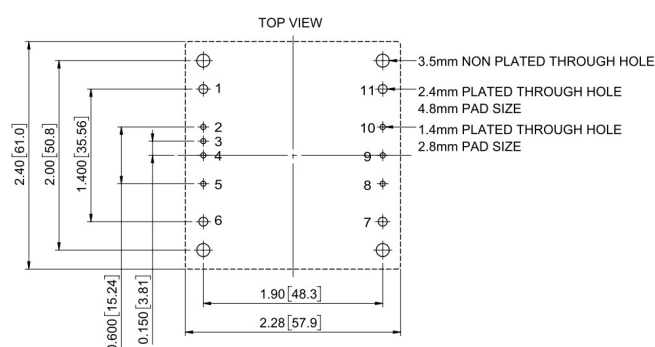


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5. Recommend Layout, PCB Footprint and Soldering Information

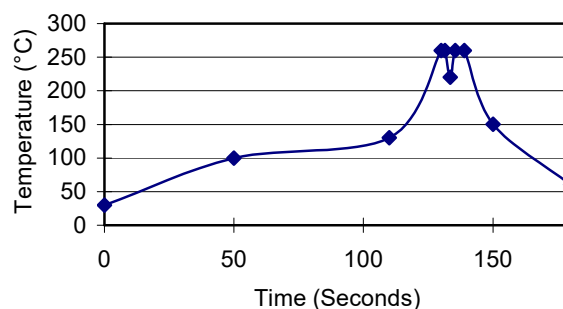
The system designer or end user must ensure that metal and other components in the vicinity of the converter meet the spacing requirements for which the system is approved. Low resistance and inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds.



Clean the soldered side of the module with a brush, prevent liquid from getting into the module. Do not clean by soaking the module into liquid. Do not allow solvent to come in contact with product labels or resin case as this may change the color of the resin case or cause deletion of the letters printed on the product label. After cleaning, dry the modules well.

The suggested soldering iron is $420 \pm 10^\circ\text{C}$ for up to 4~15 seconds (less than 90W). Furthermore, the recommended soldering profile is shown below, and PCB layout is referring to **section 10.2**.

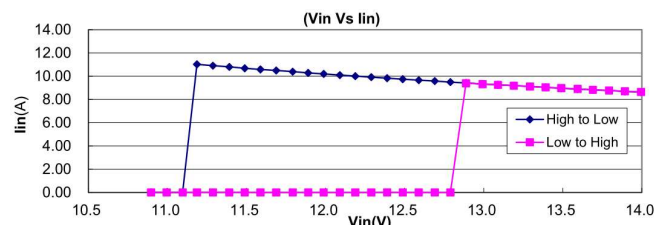
Lead Free Wave Soldering Profile



6. Features and Functions

6.1 UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the CHB200W12 series unit. The unit will shut down when the input voltage drops below a lower threshold, and the unit will operate when the input voltage goes above the upper threshold.



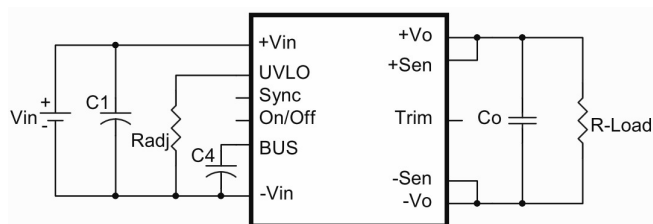
And the CHB200W12-72S series has an adjustable under voltage lockout which will shut down the converter according to following settings.

Nom. Input Voltage (VDC)	24	36	48	72	110
Turn Off Threshold (VDC)	11.0 $\pm$ 0.5	20.0 $\pm$ 1.5	27.3 $\pm$ 2.0	41.6 $\pm$ 2.0	53.0 $\pm$ 2.0
Turn On Threshold (VDC)	13.0 $\pm$ 0.5	22.0 $\pm$ 1.5	29.6 $\pm$ 2.0	44.6 $\pm$ 2.0	58.0 $\pm$ 2.0
Radj Resistor (K Ω) (UVLO to -Vin)	Open	62	34	18	10



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C1: 100uF/200V ESR<0.047Ω

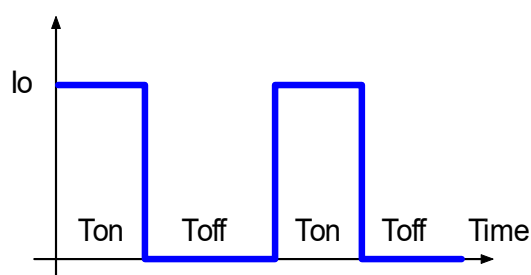
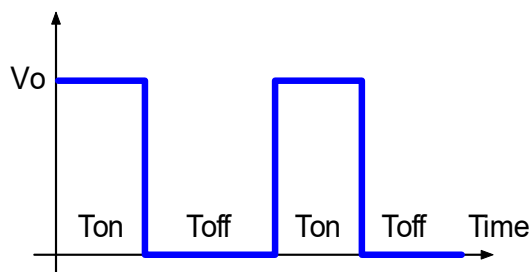
C4: 240uF (VISHAY 118 AHT series)

Note:

If need to configure UVLO turn off threshold to 11V, please make sure the output power is no more than 150W, even though the 11V operating is short duration. And the module operating between input 14Vdc to 16.5Vdc, please make sure the output power is no more than 150W.

6.2 Over Current / Short Circuit Protection

All models have internal over current and continuous short circuit protection. The unit operates normally once the fault condition is removed. At the point of current limit inception, the converter will go into hiccup mode protection.



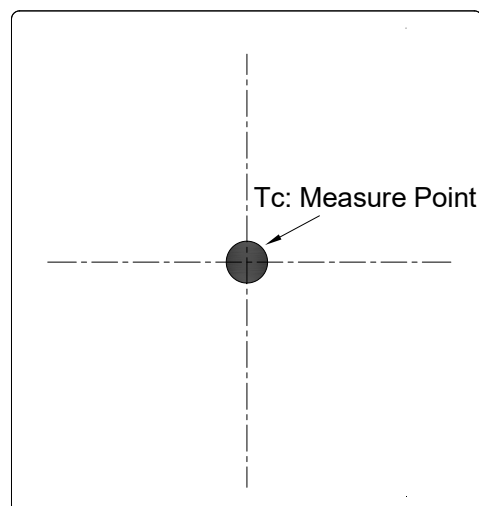
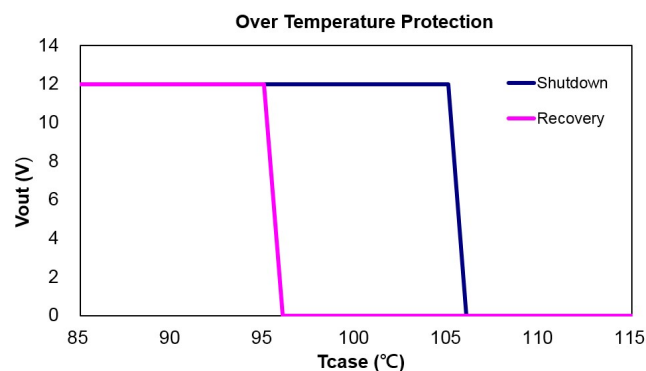
6.3 Output Over Voltage Protection

The output over voltage protection consists of circuitry that internally limits the output voltage. If more accurate output over voltage protection is required, then an external circuit can be used via the remote **on/off** pin.

Note: Please note that device inside the power supply might fail when voltage more than rate output voltage is applied to output pin. This could happen when the customer tests the over voltage protection of unit. OVP can be tested by using the **trim up** function. Consult us for more information.

6.4 Over Temperature Protection

These modules have an over temperature protection circuit to safeguard against thermal damage. Shutdown occurs with the maximum case reference temperature is exceeded. The module will restart when the case temperature falls below over temperature recovery threshold. Please measure case temperature of the center part of aluminum base plate.





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6.5 Remote On/Off

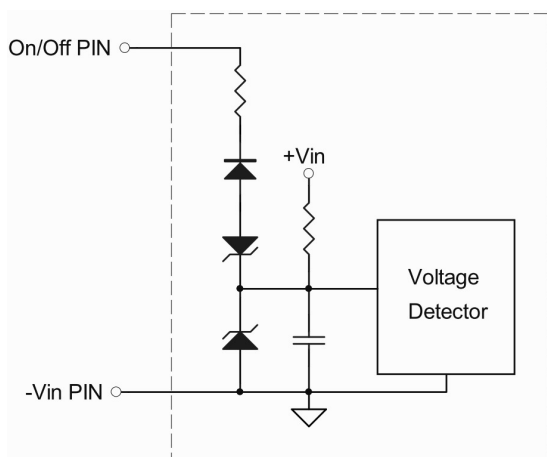
The CHB200W12 series allows the user to switch the module on and off electronically with the remote **on/off** feature. All models are available in “positive logic” and “negative logic” (optional) versions. The converter turns on if the remote **on/off** pin is high (>3.5Vdc to 160Vdc or open circuit). Setting the pin low (0 to <1.2Vdc) will turn the converter off. The signal level of the remote **on/off** input is defined with respect to ground.

If not using the remote **on/off** pin, leave the pin open (converter will be on). Converter will be turn on in positive mode.

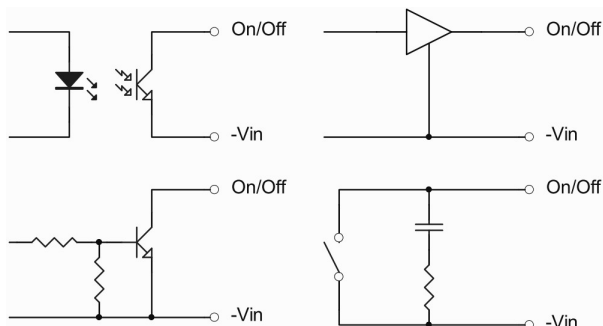
Models with part number suffix “N” are the “negative logic” remote **on/off** version. The unit turns off if the remote **on/off** pin is high (>3.5Vdc to 160Vdc or open circuit). The converter turns on if the **on/off** pin input is low (0 to <1.2Vdc). Note that the converter is off by default.

Logic State (Pin 4)	Negative Logic	Positive Logic
Logic Low	Module on	Module off
Logic High	Module off	Module on

The converter remote **on/off** circuit built-in on input side. The ground pin of input side remote **on/off** circuit is -Vin pin. Inside connection sees below.



Connection examples see below



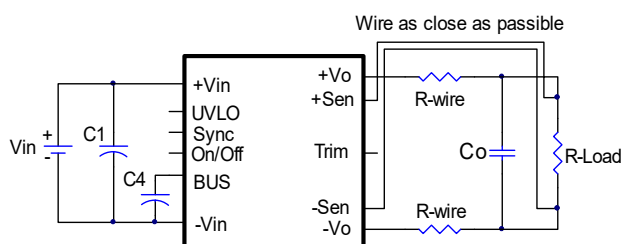
Remote On/Off Connection Examples

6.6 Output Remote Sensing

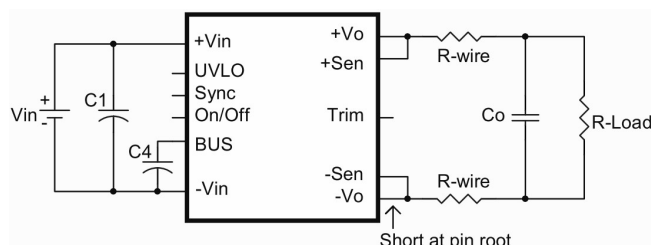
The CHB200W12 series converter has the capability to remotely sense both lines of its output. This feature moves the effective output voltage regulation point from the output of the unit to the point of connection of the remote sense pins. This feature automatically adjusts the real output voltage of the CHB200W12 series in order to compensate for voltage drops in distribution and maintain a regulated voltage at the point of load. The remote-sense voltage range is:

$$[(+V_{out}) - (-V_{out})] - [(+Sense) - (-Sense)] \leq 15\% \text{ of } V_{o_nominal}$$

When remote sense is in use, the sense should be connected by twisted-pair wire or shield wire. If the sensing patterns short, heavy current flows and the pattern may be damaged. Output voltage might become unstable because of impedance of wiring and load condition when length of wire is exceeding 400mm. This is shown in the schematic below.



If the remote sense feature is not to be used, the sense pins should be connected locally. The +Sense pin should be connected to the +V_{out} pin at the module and the -Sense pin should be connected to the -V_{out} pin at the module. Wire between +Sense and +V_{out} and between -Sense and -V_{out} as short as possible. Loop wiring should be avoided. The converter might become unstable by noise coming from poor wiring. This is shown in the schematic below.





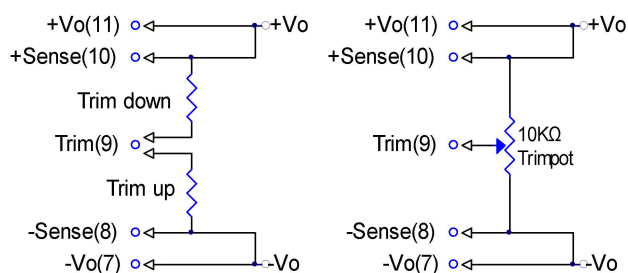
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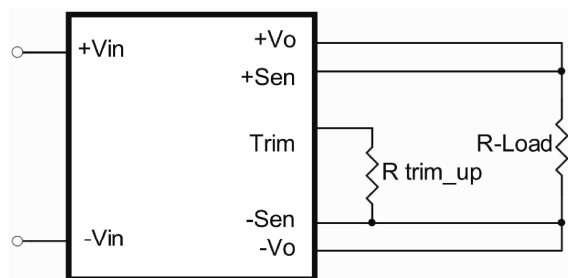
Note: Although the output voltage can be varied (increased or decreased) by both remote sense and trim, the maximum variation for the output voltage is the larger of the two values not the sum of the values. The output power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. Using remote sense and trim can cause the output voltage to increase and consequently increase the power output of the module if output current remains unchanged. Always ensure that the output power of the module remains at or below the maximum rated power. Also be aware that if $V_{o, set}$ is below nominal value, $P_{out, max.}$ will also decrease accordingly because $I_{o, max.}$ is an absolute limit. Thus, $P_{out, max.} = V_{o, set} \times I_{o, max.}$ is also an absolute limit.

6.7 Output Voltage Adjustment

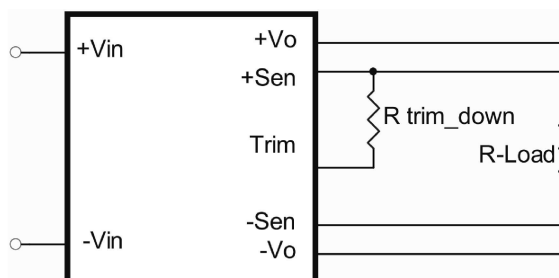
Output may be externally trimmed (-20% to +15%) with a fixed resistor or an external trim pot as shown (optional). Model specific formulas for calculating trim resistors are available upon request as a separate document.



In order to trim the voltage up or down, one needs to connect the trim resistor either between the trim pin and -Sense for trim-up or between trim pin and +Sense for trim-down. The output voltage trim range is -20% to +15%. This is shown:



Trim-up Voltage Setup



Trim-down Voltage Setup

The value of R_{trim_up} defined as:

$$R_{trim_up} = \left[\frac{V_r \times R1 \times (R2 + R3)}{R2 \times (V_o - V_{o, nom})} \right] - R_t \text{ (K}\Omega\text{)}$$

Where:

R_{trim_up} is the external resistor in K Ω

$V_{o, nom}$ is the nominal output voltage

V_o is the desired output voltage

$R1, R2, R3, R_t$ and V_r are internal to the unit and are defined in Table 1

Table 1 – Trim up and Trim down Resistor Values

Model Number	Output Voltage(V)	R1 (K Ω)	R2 (K Ω)	R3 (K Ω)	Rt (K Ω)	Vr (V)
CHB200W12-72S12	12.0	6.8	2.37	2.2	10	2.5
CHB200W12-72S15	15.0	9.1	2.32	2.49	12	2.5
CHB200W12-72S24	24.0	15	2.43	5.9	20	2.5
CHB200W12-72S48	48.0	28.7	2.4	15	43.2	2.5

For example, to trim-up the output voltage of 12V module (CHB200W12-72S12) by 15% to 13.8V, R_{trim_up} is calculated as follows:

$R1=6.8K\Omega$, $R2=2.37K\Omega$, $R3=2.2K\Omega$, $R_t=10K\Omega$, $V_r=2.5V$, $V_o=13.8V$, $V_{o, nom}=12V$

$$R_{trim_up} = \left[\frac{2.5 \times 6.8 \times (2.37 + 2.2)}{2.37 \times (13.8 - 12)} \right] - 10 = 8.21 \text{ (K}\Omega\text{)}$$



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The typical value of R_{trim_up}

Trim up (%)	12V	15V	24V	48V
	R_{trim_up} (K Ω) DC			
1%	263.17	302.45	515.62	1040.52
2%	126.59	145.22	247.81	498.66
3%	81.06	92.82	158.54	318.04
4%	58.29	66.61	113.91	227.73
5%	44.63	50.89	87.12	173.54
6%	35.53	40.41	69.27	137.42
7%	29.02	32.92	56.52	111.62
8%	24.15	27.31	46.95	92.27
9%	20.35	22.94	39.51	77.21
10%	17.32	19.44	33.56	65.17
11%	14.83	16.59	28.69	55.32
12%	12.76	14.20	24.64	47.11
13%	11.01	12.19	21.20	40.16
14%	9.51	10.46	18.26	34.21
15%	8.21	8.96	15.71	29.05

The typical value of R_{trim_down}

Trim down (%)	12V	15V	24V	48V
	R_{trim_down} (K Ω)			
1%	389.67	573.80	929.51	1715.62
2%	186.43	276.35	447.25	821.86
3%	118.69	177.20	286.50	523.94
4%	84.82	127.62	206.13	374.98
5%	64.49	97.88	157.90	285.60
6%	50.94	78.05	125.75	226.02
7%	41.27	63.89	102.79	183.46
8%	34.01	53.26	85.56	151.54
9%	28.36	45.00	72.17	126.71
10%	23.85	38.39	61.45	106.85
11%	20.15	32.98	52.68	90.60
12%	17.07	28.47	45.38	77.06
13%	14.47	24.66	39.19	65.60
14%	12.23	21.39	33.89	55.78
15%	10.30	18.56	29.30	47.27
16%	8.60	16.08	25.28	39.82
17%	7.11	13.89	21.74	33.25
18%	5.78	11.95	18.58	27.41
19%	4.59	10.21	15.76	22.18
20%	3.52	8.64	13.23	17.48

The value of R_{trim_down} defined as:

$$R_{trim_down} = R1 \times \left[\frac{V_r \times R1}{R2 \times (V_{o,nom} - V_o)} - 1 \right] - R_t \text{ (K}\Omega\text{)}$$

Where:

R_{trim_down} is the external resistor in K Ω

$V_{o,nom}$ is the nominal output voltage

V_o is the desired output voltage

$R1$, $R2$, $R3$, R_t and V_r are internal to the unit and are defined in Table 1

For example: to trim-down the output voltage of 48V module (CHB200W12-72S48) by 20% to 38.4V, R_{trim_down} is calculated as follows:

$R1=28.7K\Omega$, $R2=2.4K\Omega$, $R3=15K\Omega$, $R_t=43.2K\Omega$,
 $V_r=2.5V$, $V_o=38.4V$, $V_{o,nom}=48V$

$$\begin{aligned} R_{trim_down} &= 28.7 \times \left[\frac{2.5 \times 28.7}{2.4 \times (48 - 38.4)} - 1 \right] - 43.2 \\ &= 17.48 \text{ (K}\Omega\text{)} \end{aligned}$$



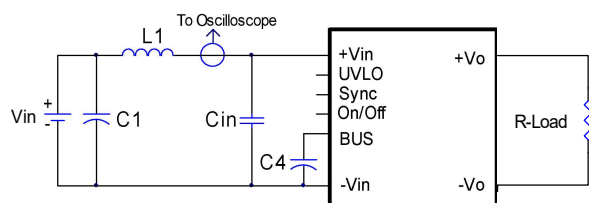
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7. Input/Output Considerations

7.1 Input Capacitance at the Power Module

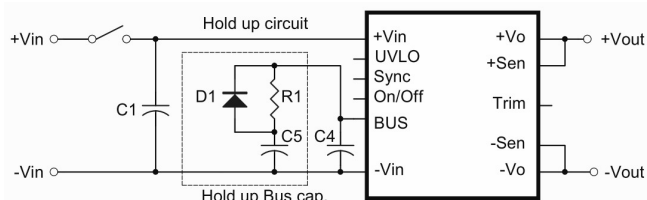
The converters must be connected to low AC source impedance. To avoid problems with loop stability source impedance should be low. Also, the input capacitors (Cin) should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR capacitors are good choice. Circuit as shown as below represents typical measurement methods for reflected ripple current. C1 and L1 simulate a typical DC source impedance. The input reflected-ripple current is measured by current probe to oscilloscope with a simulated source Inductance (L1).



L1: 12uH
 C1: 220uF ESR<0.047ohm @100KHz
 Cin: 220uF ESR<0.047ohm @100KHz
 C4: 240uF (VISHAY 118 AHT series)

7.2 Hold up Time

Hold up time is defined as the duration of time that the DC/DC converter output will remain active following a loss of input power. The BUS pin is for hold up time function. It is designed to work with an external circuit comprises C5, R1, D1. When input power supply is interrupt, the CHB200W12-72S series use the energy stored in C5 to support operation. A typical configuration shows as below.

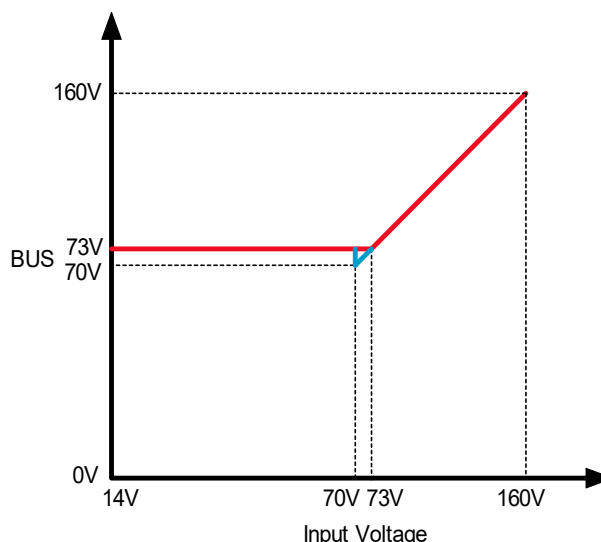


C1: 100uF/200V ESR<0.047Ω
 C4: 240uF (VISHAY 118 AHT series)
 D1: 200V/10A
 R1: 3mΩ/1W

If hold up time function is not needed, please remove the 3 components. This function provides energy that maintains the DC-DC converter in operation for 10mS and 30mS hold up time. The capacity (C5) in the application is recommended as below.

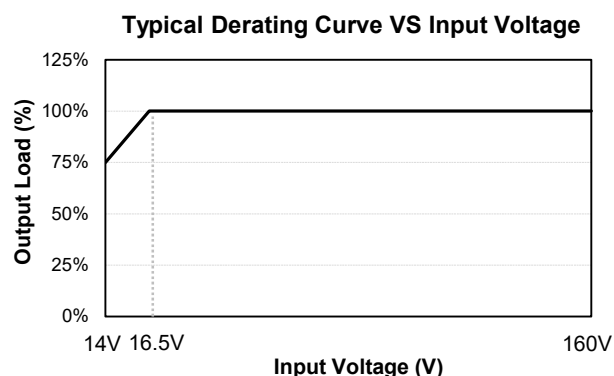
Vin C5	24V	36V	48V	72V	96V	110V
For 10 mS	2400uF	2400uF	2400uF	2400uF	820uF	560uF
For 30 mS	7200uF	7200uF	7200uF	7200uF	2460uF	1680uF

The BUS voltage and input voltage curve is showed as below. The red curve, BUS keeps stable at 73V when input voltage rise from 14V to 73V, when the input voltage is above 73V, then BUS follows input voltage. The blue curve, BUS falls with input voltage until the input voltage falls to 70V, then BUS will maintain 73V until input voltage falls below the UVLO.



7.3 Input Derating Curve

CHB200W12 series has derating by Input Voltage is required shown below.

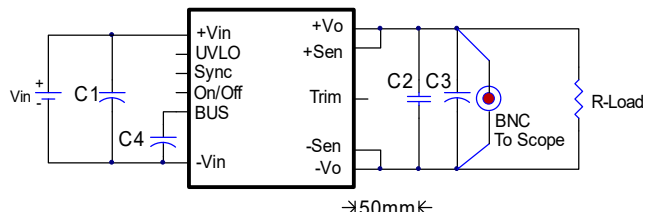




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7.4 Output Ripple and Noise



C1: 100uF/200V ESR<0.047Ω

C2: 1uF/1210 ceramic capacitor

C3: 10uF tantalum capacitor (10uF aluminum capacitor for 48V_{out})

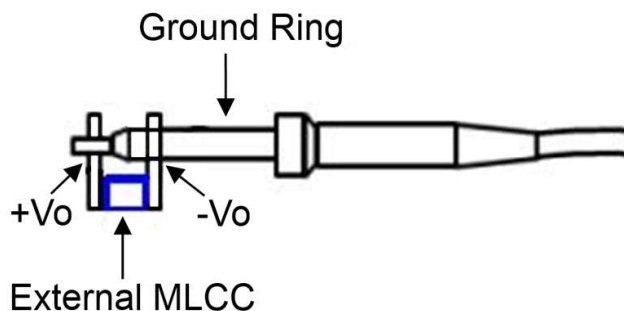
C4: 240uF (VISHAY 118 AHT series)

Output ripple and noise measured with 10uF tantalum capacitor (10uF aluminum capacitor for 48V_{out}) and 1uF ceramic capacitors across output. A 20 MHz bandwidth oscilloscope is normally used for the measurement.

The conventional ground clip on an oscilloscope probe should never be used in this kind of measurement. This clip, when placed in a field of radiated high frequency energy, acts as an antenna or inductive pickup loop, creating an extraneous voltage that is not part of the output noise of the converter.



Another method is shown in below, in case of coaxial-cable/BNC is not available. The noise pickup is eliminated by pressing scope probe ground ring directly against the -V_{out} terminal while the tip contacts the +V_{out} terminal. This makes the shortest possible connection across the output terminals.



7.5 Output Capacitance

The CHB200W12 series converters provide unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load (<100mm). PCB design emphasizes low resistance and inductance tracks in consideration of high current applications. Output capacitors with their associated ESR values have an impact on loop stability and bandwidth. Cincon's converters are designed to work with load capacitance to see specifications.

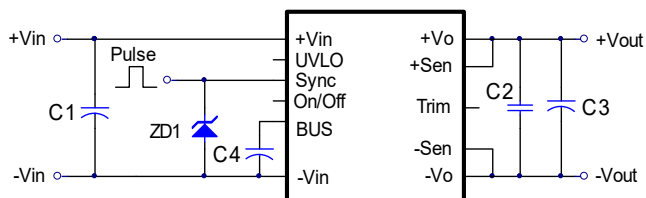


CHB200W12 Series

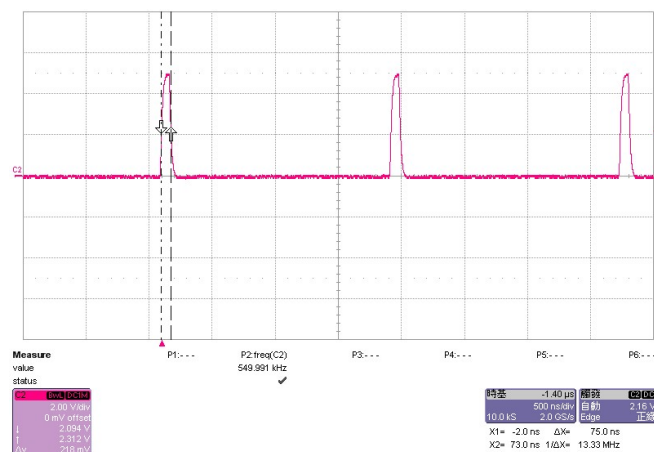
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7.6 Output Ripple Frequency Synchronized

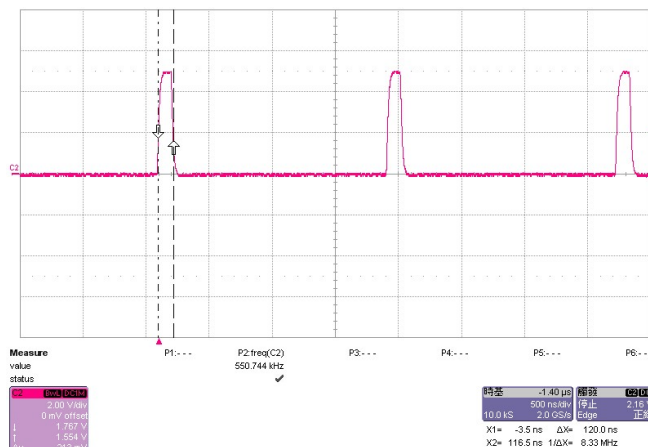
The CHB200W12-72SXX can be synchronized to an external clock by applying a narrow pulse to the Sync pin. The external clock must be at 530KHz~630KHz the switching frequency. If the external clock frequency is less than the switching frequency, the CHB200W12-72SXX will ignore the synchronizing pulses. The synchronization pulse width at the Sync pin must be a minimum of 60nS~90nS wide. The synchronizing signal must be parallel Zener 5.6V to the Sync pin. When the synchronizing pulse transitions low-to-high (rising edge). The voltage on the Sync pin must be driven to a level of 3.5V-5V. Recommend synchronization pulse width must between 75ns to 120ns. The connect setting and synchronizing pulses waveform shown as below.



- C1: 100uF/200V ESR<0.047Ω
- C2: 1uF/1210 ceramic capacitor
- C3: 10uF/100V aluminum capacitor.
- C4: 240uF (VISHAY 118 AHT series)
- ZD1: 5.6V Zener



Synchronization Pulse Width 75nS

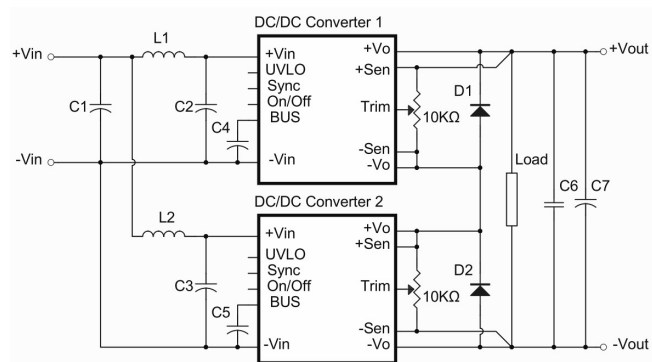


Synchronization Pulse Width 120nS

8. Series and Parallel Operation

8.1 Series Operation

Series operation is possible by connecting the outputs two or more units. Connection is shown in below. The output current in series connection should be lower than the lowest rate current in each power module.



Simple Series Operation Connect Circuit

- L1, L2: 1.0uH
- C1, C2, C3: 100uF/200V ESR<0.047Ω
- C4, C5: 240uF (VISHAY 118 AHT series)
- C6: 1uF/1210 ceramic capacitor
- C7: 10uF/200V aluminum capacitor



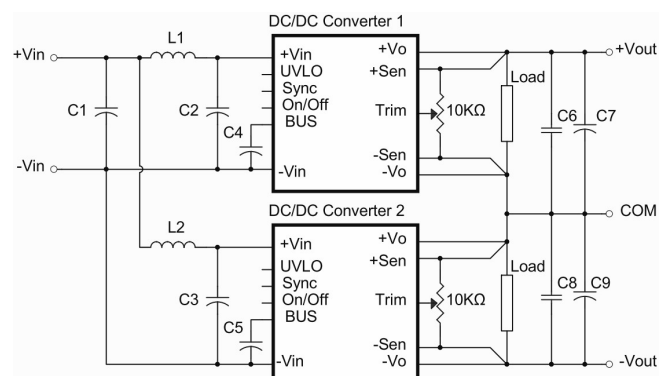
CHB200W12 Series

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Note:

1. If the impedance of input line is high, C1, C2, C3, C4, C5 capacitance must be more than above. Use more than two recommended capacitor above in parallel when ambient temperature becomes lower than -20°C.
2. Recommend Schottky diode (D1, D2) be connected across the output of each series connected converter, so that if one converter shuts down for any reason, then the output stage won't be thermally overstressed. Without this external diode, the output stage of the shut-down converter could carry the load current provided by the other series converters, with its MOSFETs conducting through the body diodes. The MOSFETs could then be overstressed and fail. The external diode should be capable of handling the full load current for as long as the application is expected to run with any unit shut down.

Series for $\pm$ output operation is possible by connecting the outputs two units, as shown in the schematic below.



Simple $\pm$ Output Operation Connect Circuit

L1, L2: 1.0uH

C1, C2, C3: 100uF/200V ESR<0.047Ω

C4, C5: 240uF (VISHAY 118 AHT series)

C6, C8: 1uF/1210 ceramic capacitor

C7, C9: 10uF/100V aluminum capacitor

Note:

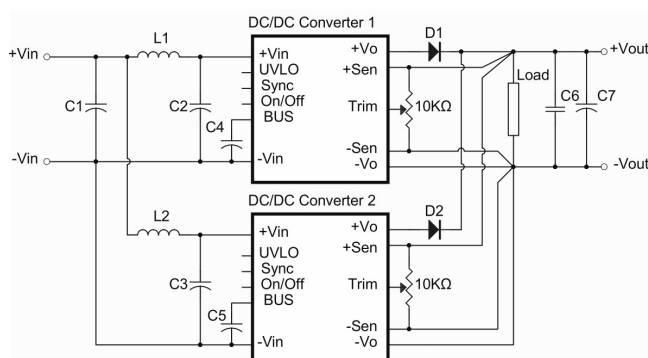
If the impedance of input line is high, C1, C2, C3, C4, C5 capacitance must be more than above. Use more than two recommended capacitor above in parallel when ambient temperature becomes lower than -20°C.

8.2 Parallel Operation

The CHB200W12 series parallel operation is **not** possible.

8.3 Redundant Operation

Parallel for redundancy operation is possible by connecting the units as shown in the schematic below. The current of each converter become unbalance by a slight difference of the output voltage. Make sure that the output voltage of units of equal value and the output current from each power supply does not exceed the rate current. Suggest use an external potentiometer to adjust output voltage from each power supply.



Simple Redundant Operation Connect Circuit

L1, L2: 1.0uH

C1, C2, C3: 100uF/200V ESR<0.047Ω

C4, C5: 240uF (VISHAY 118 AHT series)

C6: 1uF/1210 ceramic capacitor

C7: 10uF/100V aluminum capacitor

Note:

If the impedance of input line is high, C1, C2, C3, C4, C5 capacitance must be more than above. Use more than two recommended capacitor above in parallel when ambient temperature becomes lower than -20°C.



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9. Thermal Design

9.1 Operating Temperature Range

The CHB200W12 series converters can be operated within a wide case temperature range of -40°C to 100°C . Consideration must be given to the derating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn from open half brick models is influenced by usual factors, such as:

- Input voltage range
- Output load current
- Forced air or natural convection
- Heat sink optional

9.2 Convection Requirements for Cooling

To predict the approximate cooling needed for the half brick module, refer to the power derating curves in **section 9.4**. These derating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the module's temperature should be monitored to ensure it does not exceed 100°C as measured at the center of the top of the case (thus verifying proper cooling).

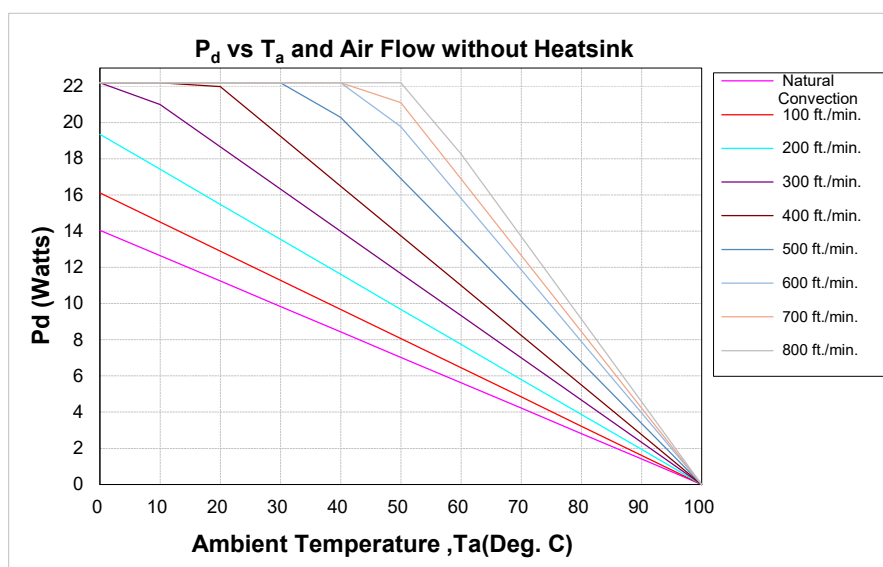
9.3 Thermal Considerations

The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The example is presented in **section 9.4**. The power output of the module should not be allowed to exceed rated power ($V_{o_set} \times I_{o_max}$).

9.4 Power Derating

The operating case temperature range of CHB200W12 series is -40°C to $+100^{\circ}\text{C}$. When operating the CHB200W12 series, proper derating or cooling is needed. The maximum case temperature under any operating condition should not exceed 100°C .

The following curve is the de-rating curve of CHB200W12 series without heat sink.



AIR FLOW RATE	TYPICAL R_{ca}
Natural Convection	
20ft./min. (0.1m/s)	7.12°C/W
100 ft./min. (0.5m/s)	6.21°C/W
200 ft./min. (1.0m/s)	5.17°C/W
300 ft./min. (1.5m/s)	4.29°C/W
400 ft./min. (2.0m/s)	3.64°C/W
500 ft./min. (2.5m/s)	2.96°C/W
600 ft./min. (3.0m/s)	2.53°C/W
700 ft./min. (3.5m/s)	2.37°C/W
800 ft./min. (4.0m/s)	2.19°C/W



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Example:

What is the minimum airflow necessary for a CHB200W12-72S12 operating at nominal line voltage, an output current of 16.7A, and a maximum ambient temperature of 50°C?

Solution:

Given: $V_{in}=72V_{dc}$, $V_o=12V_{dc}$, $I_o=16.7A$

Determine power dissipation (P_d): $P_d=P_i-P_o=P_o(1-\eta)/\eta$, $P_d=12 \times 16.7 \times (1-0.90)/0.90=22.27\text{Watts}$

Determine airflow: Given: $P_d=22.27W$ and $T_a=50^\circ C$

Check power derating curve: Minimum airflow=800ft./min.

Verify:

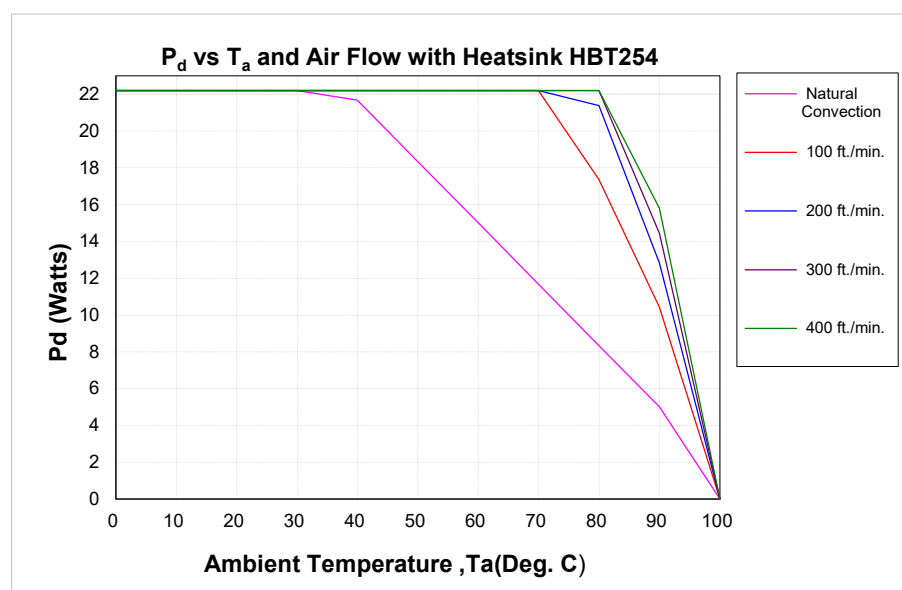
Maximum temperature rise is $\Delta T=P_d \times R_{ca}=22.27 \times 2.19=48.77^\circ C$

Maximum case temperature is $T_c=T_a+\Delta T=98.77^\circ C < 100^\circ C$

Where:

The R_{ca} is thermal resistance from case to ambient environment

T_a is ambient temperature and T_c is case temperature



AIR FLOW RATE	TYPICAL R_{ca}
Natural Convection 20ft./min. (0.1m/s)	3.00°C/W
100 ft./min. (0.5m/s)	1.44°C/W
200 ft./min. (1.0m/s)	1.17°C/W
300 ft./min. (1.5m/s)	1.04°C/W
400 ft./min. (2.0m/s)	0.95°C/W

Example with heat sink HBT254 (M-C092):

What is the minimum airflow necessary for a CHB200W12-72S12 operating at nominal line voltage, an output current of 16.7A, and a maximum ambient temperature of 70°C?

Solution:

Given: $V_{in}=72V_{dc}$, $V_o=12V_{dc}$, $I_o=16.7A$

Determine power dissipation (P_d): $P_d=P_i-P_o=P_o(1-\eta)/\eta$, $P_d=12.0 \times 16.7 \times (1-0.90)/0.90=22.27\text{Watts}$

Determine airflow: Given: $P_d=22.27W$ and $T_a=70^\circ C$

Check above power de-rating curve: Minimum airflow= 200ft./min

Verify:

Maximum temperature rise is $\Delta T=P_d \times R_{ca}=22.27 \times 1.17=26.06^\circ C$

Maximum case temperature is $T_c=T_a+\Delta T=96.06^\circ C < 100^\circ C$

Where:

The R_{ca} is thermal resistance from case to ambient environment

T_a is ambient temperature and T_c is case temperature



CHB200W12 Series

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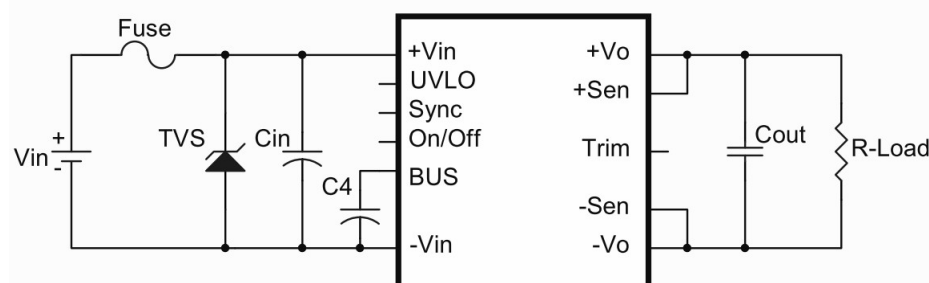
9.5 Half Brick Heat Sinks

Heat sinks assembly [refer to Datasheet-Thermal](#)

10. Safety & EMC

10.1 Input Fusing and Safety Considerations

The CHB200W12 series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 25A time delay fuse for all models. It is recommended that the circuit have a transient voltage suppressor diode (TVS) across the input terminal to protect the unit against surge or spike voltage and input reverse voltage (as shown).



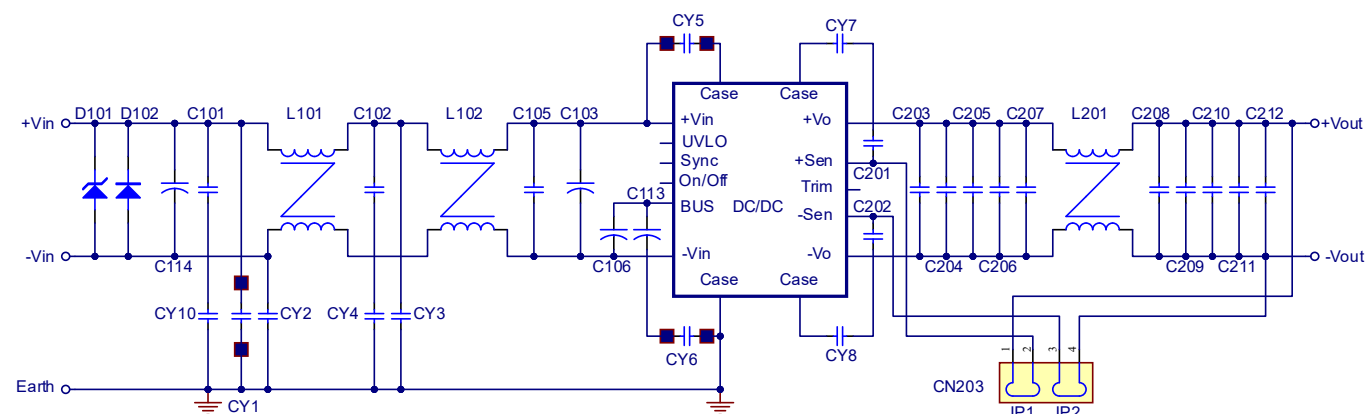
The external input capacitor (Cin) and transient voltage suppressor diode (TVS) are required if CHB200W12 series has to meet EN 61000-4-4, EN 61000-4-5.

The Cin recommended a 100uF/200V (Nippon Chemi-Con KY series) aluminum capacitor. And the TVS recommended a SMCJ180A transient voltage suppressor.

10.2 EMC Considerations

EMI Test standard: EN 55032 Class A / EN 50121-3-2:2016 Conducted & Radiated Emission

Test Condition: Input Voltage: 110V_{dc}, Output Load: Full Load



NOTE: ■ Bead core

Figure1 Connection Circuit



CHB200W12 Series

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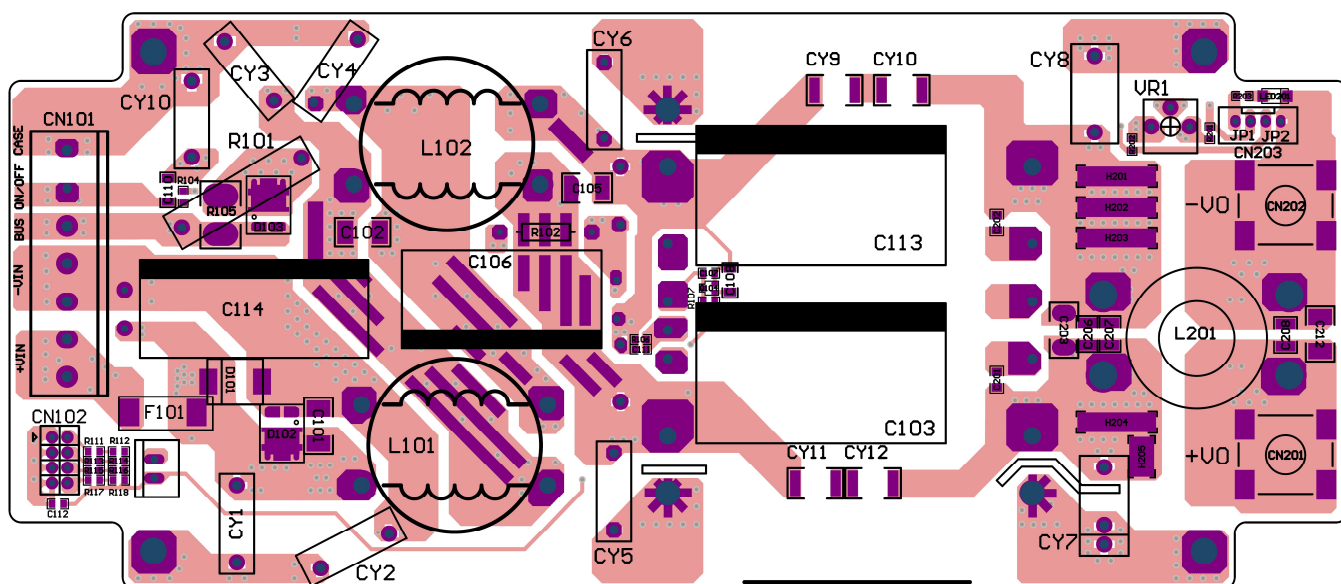


Figure2 PCB Layout Top View

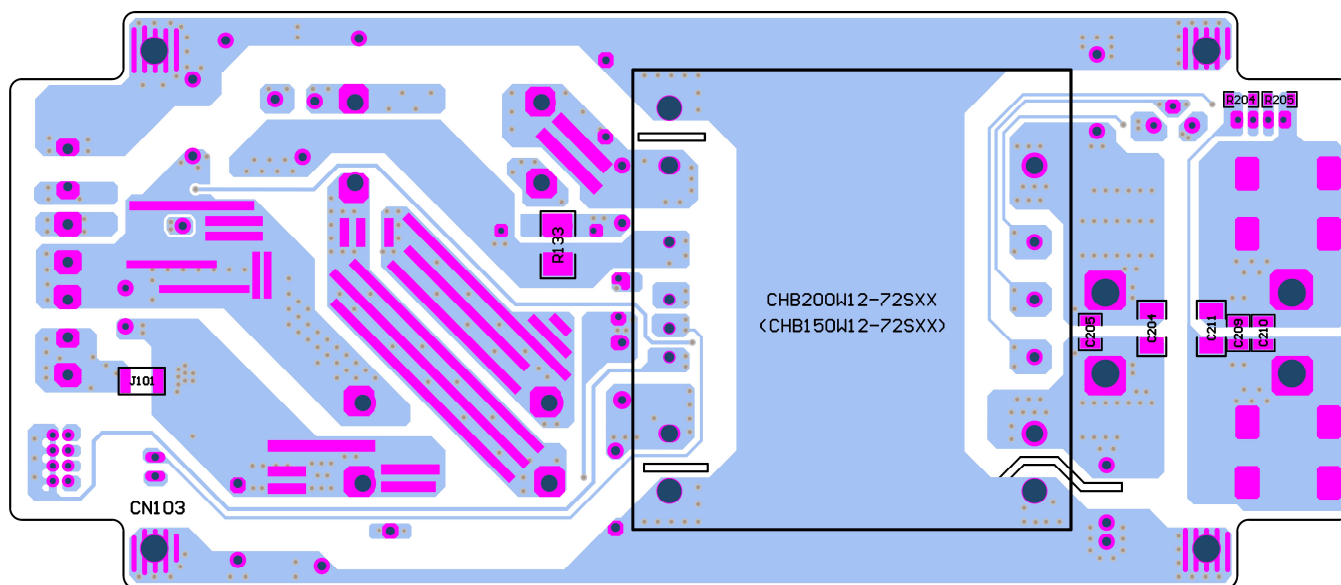


Figure3 PCB Layout Bottom View



CHB200W12 Series

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Components Value:

	Model Number			
DC/DC	CHB200W12-72S12	CHB200W12-72S15	CHB200W12-72S24	CHB200W12-72S48
C101, C102, C105	1uF/250V SMD			
C103, C113	220uF/200V, Aluminum cap. KXJ series			
C106	68uF/200V, Aluminum cap. CS series			
C114	120uF/220V, Aluminum cap. KXJ series			
C201, C202	0.1uF/100V SMD	0.068uF/50V SMD	0.1uF/100V SMD	
C203, C204	6.8uF/50V SMD			2.2uF/100V SMD
C205, C206, C207	10uF/50V SMD			2.2uF/100V SMD
C208	0.1uF/100V SMD			
C209, C210	1uF/100V SMD			
C211, C212	6.8uF/50V SMD			2.2uF/100V SMD
CY10	220pF/Y1	100pF/Y1		
CY1	100pF/Y1			220pF/Y1
CY2	220pF/Y1	100pF/Y1	NC	100pF/Y1
CY3, CY4	2200pF/Y1			
CY5, CY6	2200pF/Y1			
CY7, CY8	0.022uF/275Vac 10mm X2			
L101, L102	CMK-05 CINCON			
L201	CMK-07 CINCON		CMK-06 CINCON	
BEAD CORE	CY5, CY6 BRI 4*1.5*2 CHILISIN (G4058651007) CY1 CHB200W12-72S15 ONLY			
CN203	P110I-04 180 2.0mm			
JP1, JP2	HMJ20-02O-95BS			
Common Mode Choke Link			COMMON MODE CHOKE	

Note:

C101, C102, C105: 1812 X7R ceramic
 C103, C113: NIPPON CHEMI-CON KXJ series aluminum capacitor or equivalent
 C106: Nichicon CS series aluminum capacitor or equivalent
 C114: NIPPON CHEMI-CON KXJ series aluminum capacitor or equivalent
 C201, C202: 0805 X7R ceramic
 C203, C204, C211, C212: 1812 X7R ceramic
 C205, C206, C207, C208, C209, C210: 1206 X7R ceramic
 CY1, CY2M CY3, CY4, CY5, CY6, CY10: TDK Y1 capacitor or equivalent
 CY7, CY8: CARLI MPX Series X2 capacitor or equivalent.
 L101, L102: CMK-05 CINCON
 L201: CMK-06 CINCON for 72S24, 72S48
 CMK-07 CINCON for 72S12, 72S15
 D101: SMCJ180A, LITTELFUSE
 D102: STTH8R03DJF-TR ST
 CN203: WAFER DIP AC-DC Nylon UL 94V-0 P110I-04 180 2.0mm 4PIN TKP or equivalent
 JP1, JP2: MINI JUMPER HMJ20-02O-95BS 2.0mm TKP or equivalent: Using in CN203



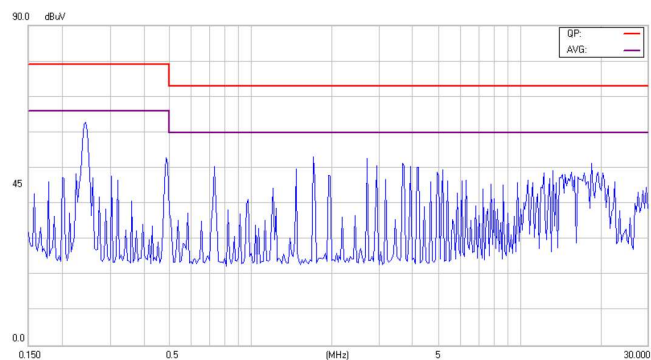
CHB200W12 Series

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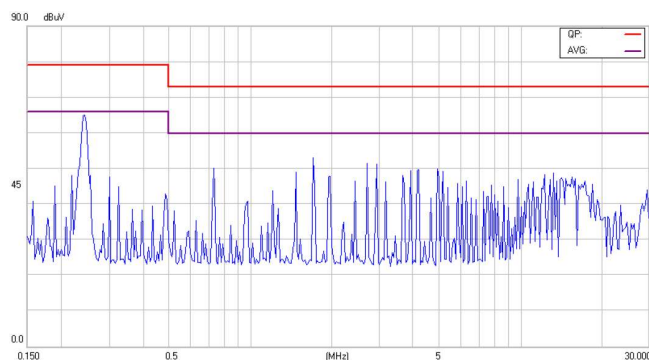
Conducted Emission (Input):

CHB200W12-72S12

Line

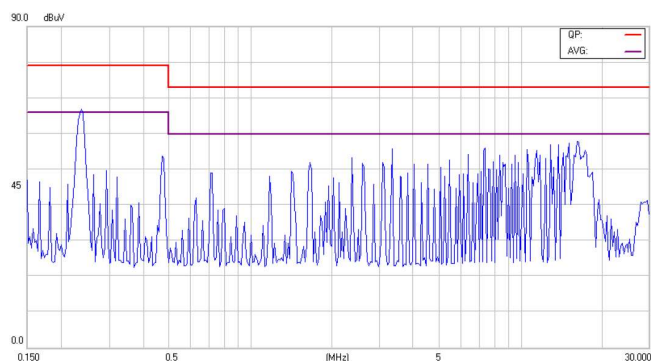


Neutral

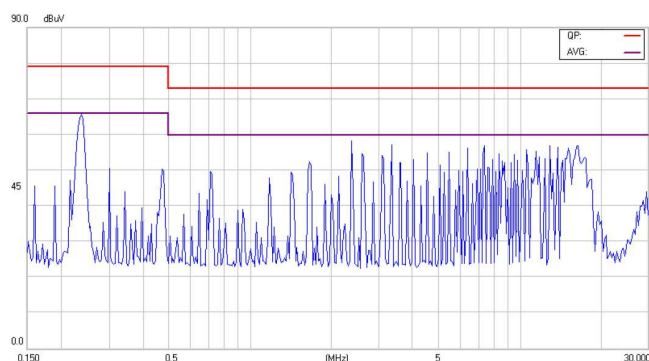


CHB200W12-72S15

Line

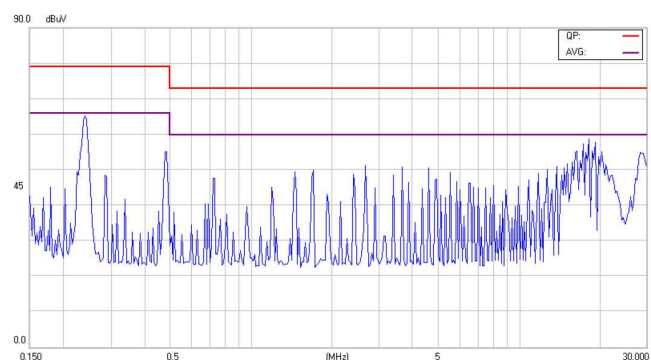


Neutral

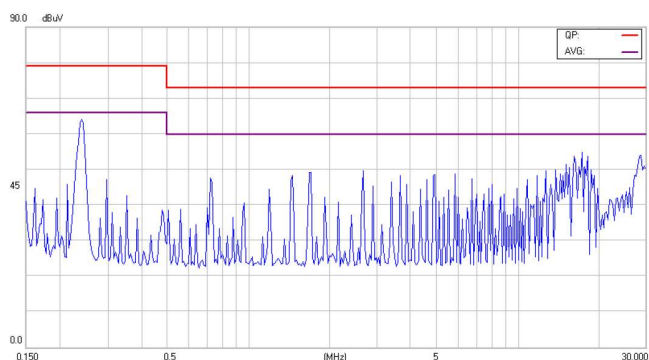


CHB200W12-72S24

Line



Neutral



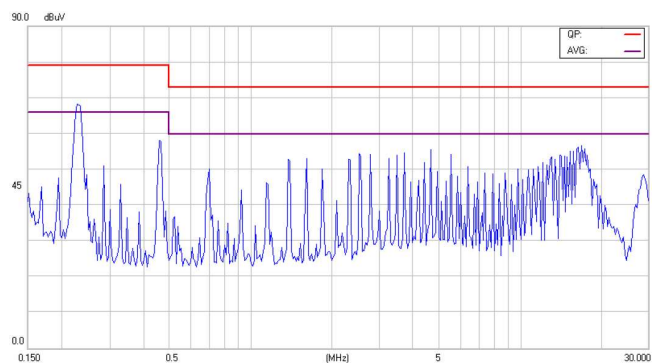


CHB200W12 Series

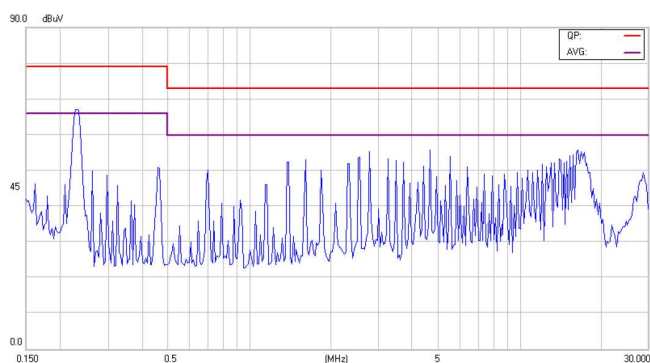
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CHB200W12-72S48

Line



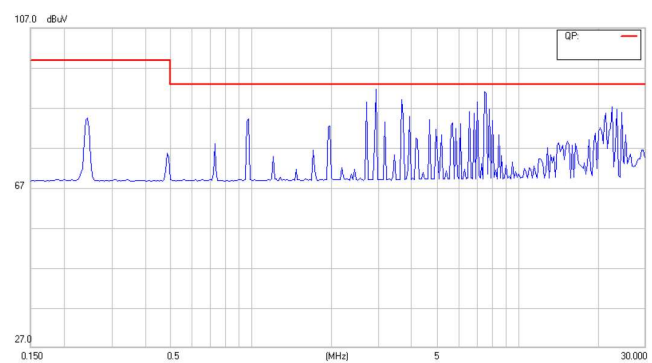
Neutral



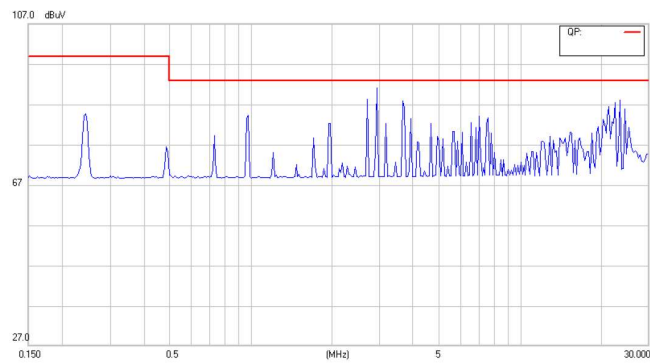
Conducted Emission (Output):

CHB200W12-72S12

Positive

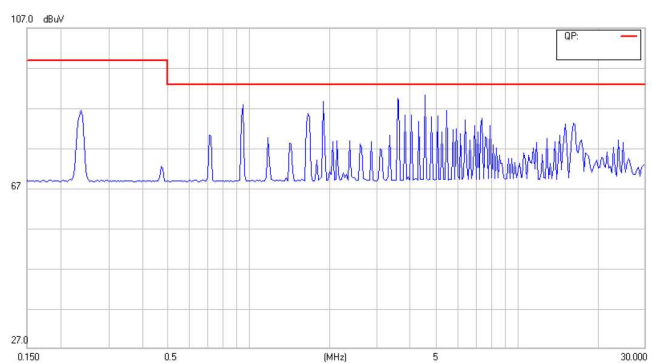


Negative

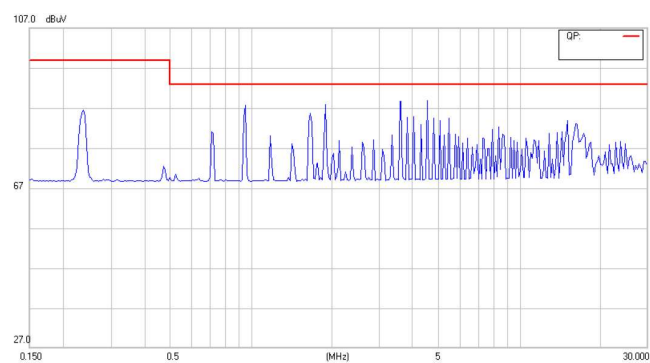


CHB200W12-72S15

Positive



Negative



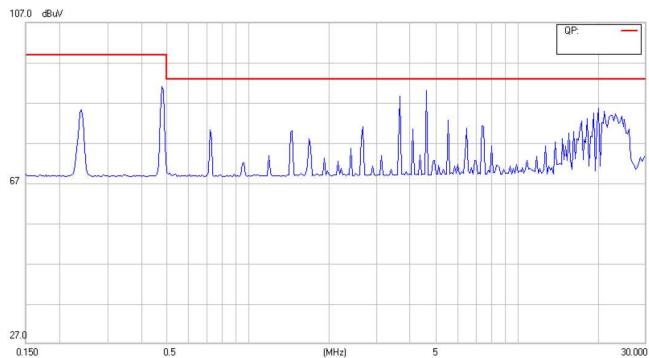


CHB200W12 Series

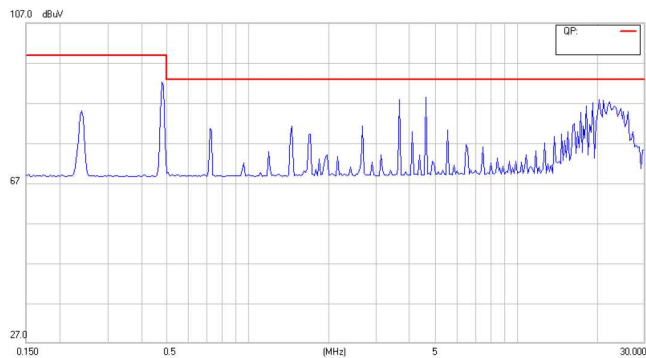
Application Note V16 October 2025

CHB200W12-72S24

Positive

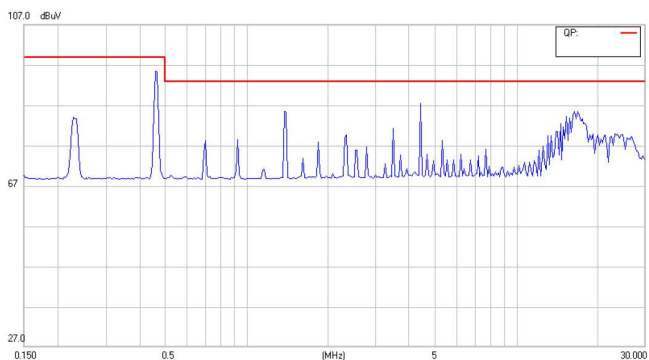


Negative

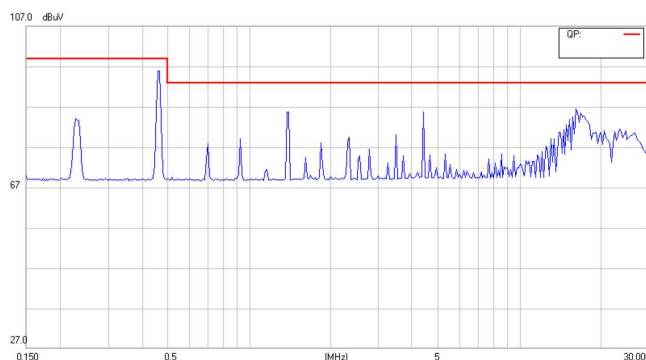


CHB200W12-72S48

Positive



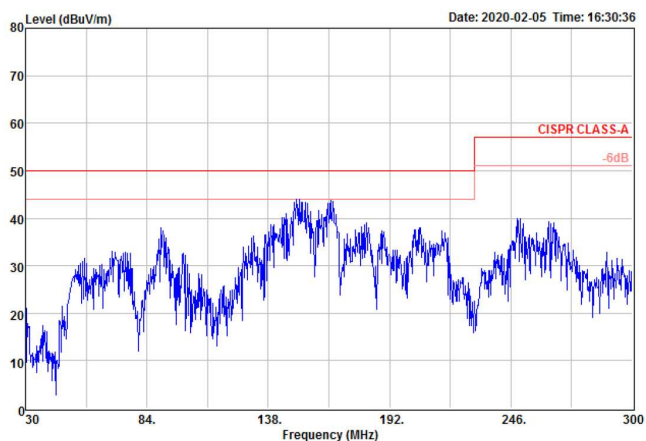
Negative



Radiated Emission:

CHB200W12-72S12

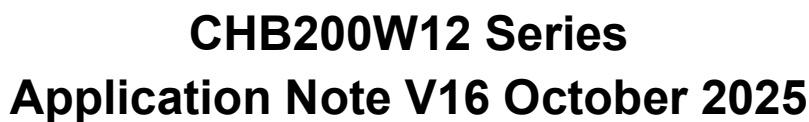
Vertical



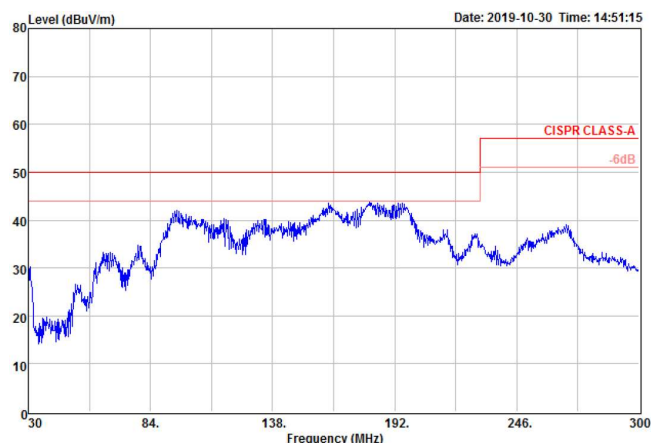
CHB200W12-72S15

Vertical

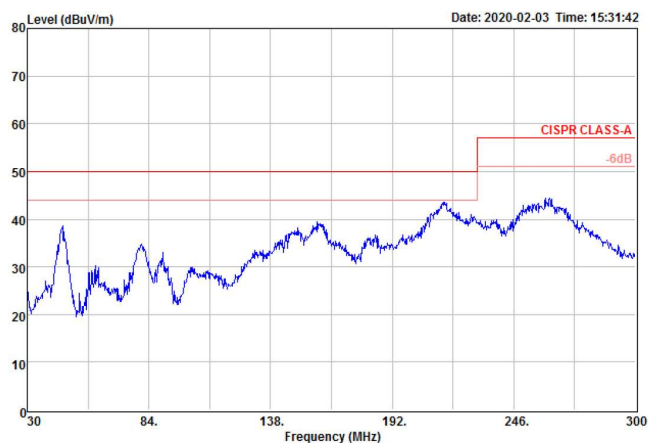




Vertical



Vertical



The diagram illustrates a 100W Class D audio amplifier circuit. It features a power supply section with +VIN and -VIN inputs, a pre-amplifier stage using a 555 timer (U1), and a push-pull output stage with MOSFETs Q1 and Q2. The circuit includes various components such as resistors (R1-R19), capacitors (C1-C9), diodes (D1-D3, ZD1-ZD7), and a TVR1. The output is taken from a terminal labeled UI-7.

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